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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/799,785	03/12/2004	Dipl.-Ing. Karl Schrodinger	16274.181	8312
22913	7590	12/26/2006	EXAMINER	
WORKMAN NYDEGGER (F/K/A WORKMAN NYDEGGER & SEELEY) 60 EAST SOUTH TEMPLE 1000 EAGLE GATE TOWER SALT LAKE CITY, UT 84111			LIU, LI	
			ART UNIT	PAPER NUMBER
			2613	
SHORTENED STATUTORY PERIOD OF RESPONSE		MAIL DATE	DELIVERY MODE	
3 MONTHS		12/26/2006	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No.	Applicant(s)	
	10/799,785	SCHRODINGER, DIPL.-ING. KARL	
	Examiner Li Liu	Art Unit 2613	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 12 March 2004.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-19 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 03/12/2004, 07/20/2004 is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date <u>08/27/04, 04/11/06</u> .	5) <input type="checkbox"/> Notice of Informal Patent Application
	6) <input type="checkbox"/> Other: _____

DETAILED ACTION

Information Disclosure Statement

1. The information disclosure statements (IDS) submitted on 08/27/2004 and 04/11/2006 are being considered by the examiner.

Preliminary Amendment

2. The preliminary amendments filed on 06/01/2004 and 07/30/2004 have been entered.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1, 3, 7, 10, 11, 12, 14 and 15 are rejected under 35 U.S.C. 102(b) as being anticipated by Geller (US 5,202,553).

1). With regard to claim 1, Geller discloses a receiver circuit, comprising:
an optical receiving device (photodiode 12 in Figure 4),
a plurality of amplifiers (the amplifiers 26 and 24 in Figure 4) which are connected to the receiving device, and

circuit means (comparator 36, switches 18, 22 and 28 in Figure 4, and Figure 5) for individually activating and deactivating the individual amplifiers (column 3, line 52-66),

wherein the amplifiers each differ from one another in at least one parameter (each amplifier has different gain and gain saturation, column 1 line 25-31, column 2 line 58-60 and column 3 line 27-47) , and

wherein only one amplifier is activated at a given point in time and the other amplifiers are deactivated (the comparator generates a control signal to switch between the first and second switchable transimpedance amplifiers, column 2, line 47-49, and column 3 line 58-66).

2). With regard to claim 3, Geller discloses wherein the amplifiers each have an input connected to the receiving device and an output (each amplifier 16 and 24 has input and output, e.g., the amplifier 16 has a input connected to switch 18 and connected to PD 12 through amplifier 14, the output of amplifier 16 is connected to another amplifier 30), and wherein the circuit means (the switches 18, 22 and 28 in Figure 4) switch the input on or off for the purpose of individually activating and deactivating the individual amplifiers.

3). With regard to claim 7, Geller discloses wherein the amplifiers each comprise a transimpedance amplifier (Figure 5, column 2 line 40).

4). With regard to claim 10, Geller discloses wherein the individual amplifiers are connected in parallel with one another (amplifier 16 and amplifier 24 are in parallel with each other in Figure 4).

5). With regard to claim 11, Geller discloses wherein the one parameter in which the individual amplifiers differ is the gain (column 2, line 58-60, one amplifier is capable of operating in low gain, another is in high gain).

6). With regard to claim 12, Geller discloses wherein the circuit means comprise a plurality of switches that are set individually (the switches 18, 22 and 28 is operated by comparator 36, column 2, line 47-49, and column 3 line 58-66).

7). With regard to claim 14, Geller discloses wherein the circuit means is adjusted via at least one control line (the output 38 from comparator 36 in Figures 4 and 5).

8). With regard to claim 15, Geller discloses wherein the receiving device comprises a photodiode (the photodiode 12 in Figure 4).

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 2, 4 and 5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Geller (US 5,202,553) in view of Bayart (US 6,069,731).

1). With regard to claim 2, Geller discloses all of the subject matter as applied to claim 1 above. And Geller discloses wherein the amplifiers each have a connection for providing a supply voltage (Figure 5). But Geller does not expressly disclose that the

circuit means switch the supply voltage on or off for the purpose of individually activating and deactivating the individual amplifiers.

However, Bayart, in the same field of endeavor, discloses a circuit means switch the supply voltage on or off for the purpose of individually activating and deactivating the individual amplifiers (Figure 7, the device CA control the energy supply to amplifiers G₁, ... G_m, in accordance with gain control word so that the amplifier G_i is the only one operating if the word G has the value i, column 3 line 1-3, and column 6 line 45-53).

Since only one amplifier is working at a given time point, it would have been obvious to one of ordinary skill in the art at the time the invention was made to apply the system or method of supplying power to only one of the fixed gain amplifiers as taught by Bayart to the system of Geller so that the power consumption and system cost can be reduced.

2). With regard to claim 4, Geller discloses all of the subject matter as applied to claim 1 above. And Geller discloses wherein the amplifiers each have an input connected to the receiving device and an output (each amplifier 16 and 24 has input and output, e.g., the amplifier 16 has a input connected to switch 18 and connected to PD 12 through amplifier 14, the output of amplifier 16 is connected to another amplifier 30).

But, Geller does not disclose wherein the circuit means switch the output on or off for the purpose of individually activating and deactivating the individual amplifiers.

However, Bayart, in the same field of endeavor, discloses a circuit means switch the output on or off for the purpose of individually activating and deactivating the

individual amplifiers (Figure 4, switch SW2, each amplifier G1, .. Gm has an input connected to and output of the first switch SW1 and has an output connected to an input of the second switch SW2, the structure of the switch SW2 is such that the inputs E₁, E₂, . . . , E_m are connected to output SC when the control signal binary word G takes the respective values 1, 2, . . . , m, column 2, line 51-55, and column 5, line 17-29).

By the switches at the output of the amplifiers, one more freedom of controlling of the amplifier system can be obtained. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use the switches at the output of the amplifiers as taught by Bayart to the system of Geller so that the a better control of different amplifier output can be obtained and also any possible interference from other amplifiers can be removed.

3). With regard to claim 5, Geller discloses all of the subject matter as applied to claim 1 above. And Geller discloses wherein wherein the amplifiers each have a current source (Figure 5). But Geller does not disclose wherein the circuit means switch the current source on or off for the purpose of individually activating and deactivating the individual amplifiers.

However, Bayart, in the same field of endeavor, discloses a circuit means switch the current source on or off for the purpose of individually activating and deactivating the individual amplifiers (Figure 7, the device CA control the energy supply to amplifiers G1, ... Gm, in accordance with gain control word so that the amplifier G_i is the only on operating if the word G has the value I, column 3 line 1-3, and column 6 line 45-53).

Since only one amplifier is working at a given time point, it would have been obvious to one of ordinary skill in the art at the time the invention was made to apply the system or method of supplying power to only one of the fixed gain amplifiers as taught by Bayart to the system of Geller so that the power consumption and system cost can be reduced.

7. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Geller (US 5,202,553) in view of Schild et al (Schild: "Amplifier Array for 12 Parallel 10 Gb/s Optical-Fiber Links Fabricated in a SiGe Production Technology", IEEE Radio Frequency Integrated Circuit Symposium, 2002, page 89-92) and Bayart (US 6,069,731).

Geller discloses all of the subject matter as applied to claim 1 above. But Geller does not disclose wherein each amplifier has a plurality of current sources, and all the current sources in an amplifier are switched on or off.

However, Schild et al discloses an amplifier that has plurality of current sources (Figures 2 and 3). Schild et al provides the amplifier with high-gain, high input-sensitivity and wide input dynamic range, low power consumption etc. (page 89, ABSTRACT).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use the amplifier as taught by Schild to the system of Geller so that the power consumption can be reduced and the gain and input-sensitivity can be improved.

Also, Bayart, in the same field of endeavor, discloses a circuit means switch the supply voltage on or off for the purpose of individually activating and deactivating the

individual amplifiers (Figure 7, the device CA control the energy supply to amplifiers G1, ... Gm, in accordance with gain control word so that the amplifier Gi is the only one operating if the word G has the value I, column 3 line 1-3, and column 6 line 45-53).

Since only one amplifier is working at a given time point, it would have been obvious to one of ordinary skill in the art at the time the invention was made to apply the system or method of supplying power to only one of the fixed gain amplifiers as taught by Bayart to the system of Geller and Schild et al so that all the current sources in an amplifier array are switched on or off, and the power consumption and system cost can be reduced.

8. Claims 8, 9 and 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Geller (US 5,202,553) in view of Schild et al (Schild: "Amplifier Array for 12 Parallel 10 Gb/s Optical-Fiber Links Fabricated in a SiGe Production Technology", IEEE Radio Frequency Integrated Circuit Symposium, 2002, page 89-92).

1). With regard to claims 8 and 9, Geller discloses all of the subject matter as applied to claim 1 above.

But Geller does not disclose wherein the amplifiers each comprise at least two amplifier cells that are connected in series (claim 8); wherein at least the first of the amplifier cells, that is connected to the receiving device comprises a transimpedance amplifier (claim 9).

However, Schild et al discloses an amplifier that comprises at least two amplifier cells that are connected in series (Figures 2, amplifiers 1, 2 and 3 are connected in series). And the first of the amplifier cells, that is connected to the receiving device

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comprises a transimpedance amplifier (Figure 3). Schild et al provides the amplifier array with high-gain, high input-sensitivity and wide input dynamic range, low power consumption etc. (page 89, ABSTRACT).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use the amplifier array as taught by Schild to the system of Geller so that the power consumption can be reduced and the gain and input-sensitivity can be improved.

2). With regard to claims 16, Geller discloses all of the subject matter as applied to claim 1 above.

But Geller does not disclose wherein the individual amplifiers are monolithically integrated in a common chip.

However, Schild et al discloses an amplifier array wherein the individual amplifiers are monolithically integrated in a common chip (Figure 1). Schild et al provides the amplifier array with high-gain, high input-sensitivity and wide input dynamic range, low power consumption etc. (page 89, ABSTRACT).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use the amplifiers integrated in a common chip as taught by Schild to the system of Geller so that the power consumption can be reduced and the gain and input-sensitivity can be improved.

9. Claim 13 is rejected under 35 U.S.C. 103(a) as being unpatentable over Geller (US 5,202,553).

Geller discloses all of the subject matter as applied to claim s 1 and 12 above.

But Geller does not expressly disclose wherein the individual switches comprise **MOS** transistors.

Although Geller doesn't specifically disclose the MOS transistors, such limitation are merely a matter of design choice and would have been obvious in the system of Geller. Geller teaches that transistors are used as the switches (Figure 5, column 4, line 9-15, line 19-26). The limitations in claim 13 do not define a patentably distinct invention over that in Geller since both the invention as a whole and Geller are directed to use transistors as the switches. Therefore, to use a MOS transistors or other kind of transistors would have been a matter of obvious design choice to one of ordinary skill in the art.

10. Claims 17 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Geller (US 5,202,553) in view of Robinson et al (US 2002/0135845).

1). With regard to claim 17, Geller discloses an optical receiver, comprising:
an optical receiving element (the photodiode 12 in Figure 4) operable to generate an electrical output signal in response to an optical input signal;
a plurality of amplifiers (the amplifiers 26 and 24 in Figure 4) having inputs coupled to an output of the optical receiving element, wherein the amplifiers each have a unique gain characteristic associated therewith (each amplifier has different gain and gain saturation, column 1 line 25-31, column 2 line 58-60 and column 3 line 27-47); and

a control circuit configured to selectively activate one of the plurality of amplifiers (the comparator generates a control signal to switch between the first and second switchable transimpedance amplifiers, column 2, line 47-49, and column 3 line 58-66).

But, Geller discloses that the control circuit is configured to selectively activate one of the amplifiers based on the **signal level** of the optical input signal, not based on the **data rate**.

However, Robinson et al discloses a receiver system in which the switching of the amplifiers is based on the **data rate** control signal (Figures 3 and 4, page 1 [0006], page 2-3, [0022]-[0023]). Robinson et al provide a fiber optic receiver that accommodates multiple data rates ([0010]).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to apply switching based on the data rate as taught by Robinson et al to the system of Geller so that the multiple data rates of the optical incoming signal can be processed by the receiver, and the receiver system can be made more flexible.

2). With regard to claim 18, Geller in view of Robinson et al discloses all of the subject matter as applied to claim 17 above. And Geller further discloses wherein the plurality of amplifiers are coupled together in parallel (amplifier 16 and amplifier 24 are in parallel with each other in Figures 4 and 5), and wherein the control circuit selectively activates one of the plurality of amplifiers via one or more switches (the comparator generates a control signal to switch between the first and second switchable

transimpedance amplifiers via switches 18, 22 and 28, Figures 4 and 5, column 2, line 47-49, and column 3 line 58-66).)

11. Claim 19 is rejected under 35 U.S.C. 103(a) as being unpatentable over Geller (US 5,202,553) and Robinson et al (US 2002/0135845) as applied to claims 17 and 18 above, and in further view of Bayart (US 6,069,731).

Geller and Robinson et al disclose all of the subject matter as applied to claims 17 and 18 above. And Geller further discloses wherein the one or more switches (the switches 18, 22 and 28 in Figures 4 and 5) comprise switches coupled between an input of the amplifiers and the output of the optical receiving element (each amplifier 16 and 24 has input and output, e.g., the amplifier 16 has a input connected to switch 18 and connected to PD 12 through amplifier 14). And Robinson et al also discloses the amplifier circuit comprises a switch for setting a bandwidth response of the post-amplifier circuit in response to a received data rate control signal.

But Geller and Robinson do not expressly disclose switches coupled between an output of the amplifiers and an output of the optical receiver, or switches coupled between a power supply and the amplifiers.

However, Bayart, in the same field of endeavor, discloses switches coupled between an output of the amplifiers and an output of the optical receiver (Figure 4, switch SW2, each amplifier G1, .. Gm has an input connected to and output of the first switch SW1 and has an output connected to an input of the second switch SW2, the structure of the switch SW2 is such that the inputs E₁, E₂, . . . , E_m are connected to output SC when the control signal binary word G takes the respective values 1, 2, . . . ,

m, column 2, line 51-55, and column 5, line 17-29). Bayart also discloses a switch circuit coupled between a power supply and the amplifiers (Figure 7, the device CA control the energy supply to amplifiers G_1, \dots, G_m , in accordance with gain control word so that the amplifier G_i is the only one operating if the word G has the value i , column 3 line 1-3, and column 6 line 45-53).

By the switches at the output of the amplifiers or for power supplying, one more freedom of controlling of the amplifier system can be obtained. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use the switches at the output of the amplifiers as taught by Bayart to the system of Geller and Robinson so that a better control of different amplifier output can be obtained, the power consumption and system cost can be reduced and also any possible interference from other amplifiers can be removed.

Conclusion

12. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Eumurian et al (US 4,481,676) discloses transceiver system for variable-rate transmission via optical fiber links.

Lee et al (US 6,362,911) discloses an optical receiver using two amplifiers having different bandwidth.

Mullrich et al (Mullrich et al: "High-gain transimpedance amplifier in InP-based HBT technology for the receiver in 40 Gb/s optical-fiber TDM links", IEEE, 21st Annual

Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, Monterey, CA, USA, 1999, page 99-102) discloses a high-gain transimpedance amplifier.

13. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Li Liu whose telephone number is (571)270-1084. The examiner can normally be reached on Mon-Fri, 8:00 am - 5:30 pm, alternating Fri off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ken Vanderpuye can be reached on (571)272-3078. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Li Liu
December 19, 2006



KENNETH VANDERPUYE
SUPERVISORY PATENT EXAMINER